In the claims:

Please cancel non-elected Claims 6-11 without prejudice.

Please amend the claims as follows:

1. (amended) A method for controlled electroless plating of uniform metal layers onto exposed metallizations in integrated circuits positioned on the active surface of semiconductor wafers, comprising the steps of:

maintaining a plurality of said wafers approximately parallel to each other at predetermined distances by supporting an edge of each said wafer between a plurality of support means;

immersing said wafers into an electroless plating solution flowing in laminar motion at constant speed substantially parallel to said active surface of said wafers;

rotating each of said wafers at constant speed and synchronously with each other by turning each of said plurality of support means; and

creating periodic relative motion in changing directions between said plating solution and said wafers, thereby uniformly plating layers onto said exposed metallizations by controlled electroless deposition.

Please add the following new claims:

12. (new) A method of electroless plating of features on a semiconductor wafer, comprising the steps of:

A2

supporting said wafer with a plurality of support means; rotating said wafer by rotating each of said plurality of support means; and flowing plating solution over the surface of said wafer.

13. (new) The method of Claim 12, wherein said step of supporting said wafer comprises supporting said wafer with a plurality of rollers.

- 14. (new) The method of Claim 13, wherein said step of supporting comprises supporting said wafer in a groove in each of said rollers in said plurality.
- 15. (new) The method of Claim 12, wherein said step of rotating said wafer comprises turning a gear on each of said support means by turning a central sun gear engaged with said gear on each of said support means.
- 16. (new) The method of Claim 12, further comprising the step of immersing said wafer and said support means in said plating solution.
- 17. (new) The method of Claim 16, wherein said step of immersing said wafer comprises immersing said wafer and said support means in a tank and said step of flowing plating solution comprises flowing said solution from the bottom of said tank to the top of said tank.

A2

18. (new) A method of electroless plating of features on a semiconductor wafer, comprising the steps of:

supporting said wafer with a plurality of parallel rollers, each of said rollers having grooves for supporting an edge of said wafer;

immersing said wafer and said rollers in plating solution;
rotating said wafer by rotating said parallel rollers; and
flowing plating solution over the surface of said wafer during said step of
rotating said wafer.

- 19. (new) The method of Claim 18, wherein said step of rotating said wafer comprises turning a gear on each of said support means by turning a central sun gear engaged with said gear on each of said support means.
- 20. (new) The method of Claim 18, wherein said step of immersing said wafer comprises immersing said wafer and said support means in a tank and said step